

ABSTRACT OF THE DISCLOSURE

The present invention provides a semiconductor device including a first semiconductor element to be bonded to a wiring board in a flip-chip bonding manner, a resin peripheral wall provided on the wiring board in such a manner as to surround the first semiconductor element, a sealing resin poured so as to fill a space surrounded by the resin peripheral wall and then hardened, and a second semiconductor element provided in such a manner that a back surface thereof is fixed on an upper surface of the sealing resin and a electrode provided on a front surface thereof is connected to a segment of wiring on the wiring board by means of a bonding wire, and provides a method of fabricating the semiconductor device. With this configuration, since semiconductor elements of arbitrary outer sizes can be stacked to each other without any limitation by the outer sizes of the semiconductor elements, even in a combination of any outer shapes of semiconductor elements vertically stacked to each other, the semiconductor elements can be mounted at a high density.

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